Excellent Text On Digital Systems Testing

This updated printing of the leading text and reference in digital systems testing and testable design provides comprehensive, state-of-the-art coverage of the field. Included are extensive discussions of test generation, fault modeling for classic and new technologies, simulation, fault simulation, design for testability, built-in self-test, and diagnosis. Complete with numerous problems, this book is a must-have for test engineers, ASIC and system designers, and CAD developers, and advanced engineering students will find this book an invaluable tool to keep current with recent changes in the field.

My Personal Review:
This book is not ideal for engineers with very little exposure to digital circuitry. A prior course in digital circuits is essential before getting the most from this text. Once you have a fundamental background, this book is excellent in extending your design knowledge into the testing world. A great design which cannot be tested isn't worth much. This book will reinforce this concept.

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